



INDIAN INSTITUTE OF INFORMATION TECHNOLOGY, KALYANI

(An Institute of National Importance under MoE, Govt. of India)

WEBEL IT Park, Near Buddha Park, Dist. Nadia, P.O. Kalyani,

PIN - 741235, West Bengal.

Advt. No: IITK/Acad/M.Tech /2026/08

Date: 04.08.2026

M.Tech Admission (without GATE) in “VLSI and Embedded Systems” 2026

The Indian Institute of Information Technology Kalyani (IIIT Kalyani) invites applications from eligible non-GATE candidates for admission to its M.Tech. programme in VLSI & Embedded Systems.

No. of Seats: Tentative vacancy is Twenty-Five (25). The total number of seats available through CCMT Counselling 2026 for GATE-qualified candidates at IIIT Kalyani (M.Tech. programme in VLSI & Embedded Systems) is Thirty (30). As per the CCMT Counselling 2026 schedule, the exact number of seats remaining vacant after the completion of the counselling process will be known on 12th August 2026. Subject to the availability of vacant seats, admissions to the M.Tech. programme in VLSI & Embedded Systems at IIIT Kalyani will subsequently be offered to eligible non-GATE candidates. Reservation for SC/ST/OBC/PwD/EWS categories shall be implemented in accordance with the Government of India norms.

About the Institute: Set up in the year 2014, IIIT Kalyani is one of the 25 IIITs established by the Ministry of Education, Government of India, under the *Not for profit Public Private Partnership* (N-PPP) model. A Memorandum of Understanding (MoU) was signed between the Government of India, Government of West Bengal, and three Industry Partners, namely, Coal India Limited, Rolta Foundation, and WBEIDC Ltd. (Webel) for setting up the institute at Kalyani, with the stakes of the partners at a ratio of 50:35:15 respectively. The institute has been declared as an *Institute of National Importance (INI)* as per *The Indian Institutes of Information Technology (Public-Private Partnership) Act, 2017*. The institute currently operates from the Webel IT Park building at Kalyani. The permanent campus of the institute is coming up at Kalyani, right on the Kalyani expressway and it is all set to move to its permanent campus at an early date.

About the Program: The M.Tech program in *VLSI and Embedded Systems* was introduced at the institute in the year of 2025. Spanning over a duration of four semesters, the program aims to equip the students with both theoretical and practical knowledge in the domain. The institute is a part of the “*Chip to Startup (C2S)*” program of the Ministry of Electronics and Information Technology (*MeitY*), Govt. of India. Under this program, FPGAs and VLSI CAD tools from all the major vendors are available to the students and researchers of the institute, with sufficiently large number of licences. Fabrication activities are also supported under the scheme.

Eligibility: B.E./B. Tech. in Electronics and Communication Engineering, Computer Science and Engineering, Electrical Engineering or any other relevant disciplines.

Course Structure: The course is distributed over four semesters, as noted next.

- **Semester I:** Analog IC Design (with laboratory), Digital IC Design (with laboratory), Advanced Semiconductor Devices, Embedded Systems, Elective-I
- **Semester II:** Low Power VLSI, Mixed-Signal IC Design (with laboratory), VLSI Technology, Embedded System Laboratory, Elective-II, Elective-III
- **Semester III:** Project-I
- **Semester IV:** Project-II

Financial Support: IIIT Kalyani, being a PPP mode institute will not be able to provide any stipend to the students. However, they may apply for partial support (subject to availability), in the form of *Teaching Assistantship* in their 3rd and 4th semesters.

Placement Support: The institute has a fully structured Training and Placement Cell running for the undergraduate students. The facility will be extended to the M. Tech. students as well.

Fee Structure: The fee to be paid by the newly admitted students consists of two components, as noted below.

- Academic Fees (Same for all students GEN/SC/ST/OBC/EWS/PwD)
- Hostel Fees (*optional, to be paid only by the students availing hostel facility*)

Academic Fees:

A. Institute Fee (Per Semester)

<i>Head</i>	<i>Amount (INR)</i>	<i>Remarks</i>
Tuition Fee	50000	Per semester
Students' Activities Fee	1430	With 10% increase every year
Infrastructural Maintenance Fee	550	With 10% increase every year
Examination Fee	1500	Per semester
Accident Medical Insurance Fee	1500	Per semester
Total	54,980	

B. One-time Payable during Admission

<i>Head</i>	<i>Amount (INR)</i>	<i>Remarks</i>
Registration Fee	1500	One time
Identity Card Fee	200	One time
Institute Development Fee	5000	One time
Alumni Registration Fee	2000	One time
Institute Caution Money	10000	One time, refundable
Total	18700	

Hostel Fees:

<i>Head</i>	<i>Amount (INR)</i>	<i>Remarks</i>
Establishment & Maintenance Fee*	7000	Per semester
Rent	25000	Per semester
Mess Charges in Advance	25000	Per semester, on actual basis
Electricity and Internet Charges	On actual	
Total	57,000	

*With 10% increase every year

How to Apply: Filled in application form (available at <http://iiitkalyani.ac.in>) along with all necessary documents should be sent as a single PDF file to soumen@iiitkalyani.ac.in with subject title "Application for Admission to M. Tech. in VLSI & Embedded Systems" on or before 17th August, 2026. Hardcopy of the same application should reach the following address by 19th August, 2026 with entitled envelope as: "Application for Admission to M. Tech. in VLSI & Embedded Systems". **Address:** Registrar, IIIT Kalyani, WEBEL IT Park (Phase-I), Kalyani, West Bengal - 741235

Contact Details: soumen@iiitkalyani.ac.in, 9874482354