Advt. No: IIITK/Acad/M.Tech /2025/15   **Date: 06.08.2025**

M.Tech Admission (without GATE) in "VLSI and Embedded Systems" -2025

About the Institute: Set up in the year 2014, IIIT Kalyani is one of the 25 IIITs established by the Ministry of Education, Government of India, under the *Not for profit Public Private Partnership* (N-PPP) model. A Memorandum of Understanding (MoU) was signed between the Government of India, Government of West Bengal, and three Industry Partners, namely, Coal India Limited, Rolta Foundation, and WBEIDC Ltd. (Webel) for setting up the institute at Kalyani, with the stakes of the partners at a ratio of 50:35:15 respectively. The institute has been declared as an *Institute of National Importance* (*INI*) as per *The Indian Institutes of Information Technology (Public-Private Partnership) Act*, 2017. The institute currently operates from the Webel IT Park building at Kalyani. The permanent campus of the institute is coming up at Kalyani, right on the Kalyani expressway. More than 90% of construction of the permanent campus is over.

About the Program: The M.Tech program in *VLSI and Embedded Systems* is being introduced at the institute from the year 2025. Spanning over a duration of four semesters, the program aims to equip the students with both theoretical and practical knowledge in the domain. The institute is a part of the "*Chip to Startup* (*C2S*)" program of the Ministry of Electronics and Information Technology (*MeitY*), Govt. of India. Under this program, FPGAs and VLSI CAD tools from all the major vendors are available to the students and researchers of the institute, with sufficiently large number of licences. Fabrication activities are also supported under the scheme.

Eligibility: B.E./B.Tech. in Electronics and Communication Engineering, Computer Science and Engineering, Electrical Engineering or related disciplines.

No. of Seats: 28 (General: 12, General-PWD: 01, SC: 02, ST: 02, OBC: 08, EWS: 03)

Course Structure: The course is distributed over four semesters, as noted next.
- **Semester I:** Analog IC Design (with laboratory), Digital IC Design (with laboratory), Advanced Semiconductor Devices, Embedded Systems, Elective-I
- **Semester II:** Low Power VLSI, Mixed-Signal IC Design (with laboratory), VLSI Technology, Embedded System Laboratory, Elective-II, Elective-III
- **Semester III:** Project-I
- **Semester IV:** Project-II

Financial Support: IIIT Kalyani, being a PPP mode institute will not be able to provide any stipend to the students. However, they may apply for partial support (subject to availability), in the form of *Teaching Assistantship* in their 3rd and 4th semesters.

Placement Support: The institute has a fully structured Training and Placement Cell running for the undergraduate students. The facility will be extended to the M.Tech students as well.

Fee Structure: The fee to be paid by the newly admitted students consists of two components, as noted below.

- Academic Fees (Same for all students GEN/SC/ST/OBC/EWS/PWD)
- Hostel Fees (*optional, to be paid only by the students availing hostel facility*)

1. Academic Fees:
A. Institute Fee (Per Semester)

Head	Amount (INR)	Remarks
Tuition Fee	50000	Per semester
Students' Activities Fee	1430	With 10% increase every year
Infrastructural Maintenance Fee	550	With 10% increase every year
Examination Fee	750	Per semester
Grade Card Fee	200	Per semester
Accident Medical Insurance Fee	750	Per semester
Total	53680	

B. One-time Payable during Admission

Head	Amount (INR)	Remarks
Registration/Admission Fee	500	One time
Identity Card Fee	200	One time
Institute Development Fee	5000	One time
Alumni Registration Fee	2000	One time
Library Caution Money	1000	One time, refundable
Institute Caution Money	2000	One time, refundable
Total	10700	

2. Hostel Fees:

Head	Amount (INR)	Remarks
Admission Fee	1000	One time
Caution Money	3000	One time, refundable
Establishment & Maintenance Fee*	2200	Per semester
Rent in Advance*	24420	Per semester
Transport Fee*	6600	Per semester
Overhead Fee*	4400	Per semester
Mess Charges in Advance	20000	Per semester, on actual basis
Electricity and Internet Charges	On actual	
Total	61620	

*With 10% increase every year

How to Apply: Filled application form (available at http://iiitkalyani.ac.in) with all necessary documents should be sent as softcopy (a single PDF) to sanjoy@iiitkalyani.ac.in (on or before 22nd August, 2025). Hardcopy applications should reach the following address by 22nd August, 2025. The envelope should be superscribed as: "Application for admission to the M.Tech (VLSI & Embedded Systems) program".

Address:

Registrar
IIIT Kalyani
WEBEL IT Park (Phase-I)
Kalyani
West Bengal - 741235

Contact Details:

Any query in this regard may be sent to the following.

Email: sanjoy@iiitkalyani.ac.in
Phone: 9832377258

Registrar